

Project Completion Demonstration - Final PSDR Checkoff Requirements – Fall 2023

- The project must be fully integrated and in the final packaging. See the PSDR Policy and the Project Hardware Policy to ensure that you have met all the requirements.
- Only Joe Bougher or Dr. Walter (preferably both) can checkoff the Final PSDRs. Your mentor TA should also be there if possible. When you are ready, **send us an email to schedule**.
- At the time we are doing the Final PSDR Checkoff, we will be evaluating (and taking photos) for your integration and packaging grade. We will also be checking off any Design Bonuses (stretch goals) at this time.
- The **deadline for completing the Final PSDR Checkoff is Friday, December 1 at 5:00pm** (the Friday of Week 15 – the week before Quiet Week).
- Teams may earn an early completion bonus for completing the Final PSDR Checkoff **before Week 15**. 1% bonus for each week early. E.g., if you complete the Final PSDR checkoff anytime in Week 14, each team member will receive a 1% bonus on their final course grade. In Week 13, 2%. Week 12, 3%. Note that a maximum of 5% can be earned as an early completion bonus.
- Not recommended, but if you cannot get some of your final PSDRs to work during the checkoff, you may redo the entire checkoff at a later time but **before the Final PSDR Checkoff deadline of December 1 at 5:00pm**. However, you must redo the full process (see below) for all 5 final PSDRs (and any early completion bonus will be based on the final checkoff date).

Project Completion Demonstration - Final PSDR Checkoff Process:

1. The project must be fully integrated and in the final packaging.
2. We will review how we will verify the completion of each of your five PSDRs and any design bonuses.
3. You will connect for programming and download your code onto your microprocessor on each of your PCBs (removing or opening your final packaging as necessary). We want to see your code and the IDE you are using.
4. You will disconnect your programming cables from your PCB(s) (closing or reattaching your final packaging as necessary).
5. You will power off – power on your PCB(s) (project).
6. You may reset your microcontroller(s).
7. We will verify the completion of each of your five PSDRs and any design bonus stretch goals.
8. The PSDR grade will be either a complete or not complete for each PSDR and design bonus.
9. We will take photos at this time for the project integration and packaging grade.